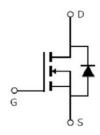


V _{DSS}	40V	
R _{DS} (on)	s(on) 2.4mΩ(typ.)	
I _D	200A	







Advanced MOSFET process technology
Special designed for PWM, load switching and
general purpose applications
Ultra low on-resistance with low gate charge
Fast switching and reverse body recovery
175 operating temperature



It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

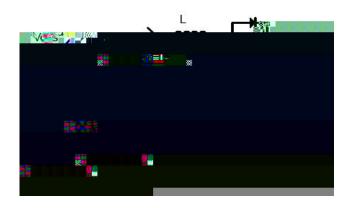
I _D @ TC = 25°C	Continuous Drain Current, V _{GS} @ 10V	200	A	
I _D @ TC = 100°C	Continuous Drain Current, V _{GS} @ 10V	135		
I _{DM}	Pulsed Drain Current	750		
P _D @TC = 25°C	Power Dissipation	220	W	
	Linear Derating Factor	1.5	W/°C	
V _{DS}	Drain-Source Voltage	40	V	
V _{GS}	Gate-to-Source Voltage	± 24	V	
E _{AS}	Single Pulse Avalanche Energy @ L=0.3mH	912	mJ	
I _{AS}	Avalanche Current @ L=0.3mH	78	Α	
T _J T _{STG}	Operating Junction and Storage Temperature Range	-55 to +150	°C	

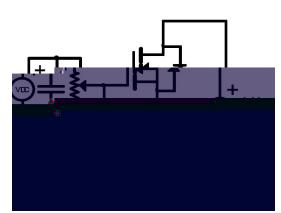


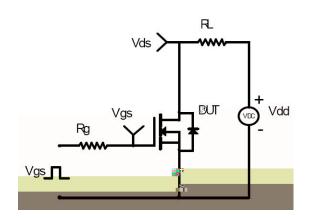
Rejc	Junction-to-case	_	0.62	/W
R _{θJA}	Junction-to-ambient (t ≤ 10s)	_	60	/W
	Junction-to-Ambient (PCB mounted, steady-state)	_	40	/W

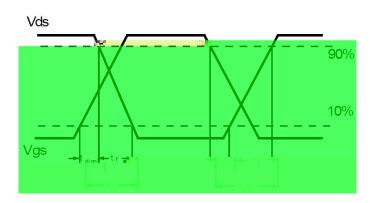
 $@T_A=25$ unless other











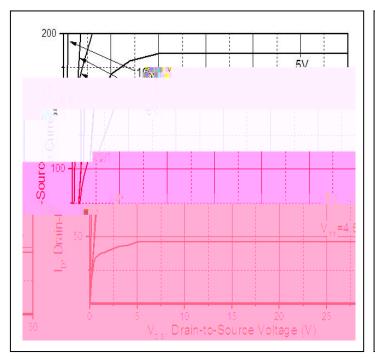
Calculated continuous current based on maximum allowable junction temperature. Package limitation current is 75A.

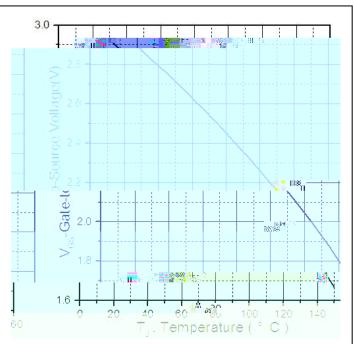
Repetitive rating; pulse width limited by max junction temperature.

The power dissipation PD is based on max junction temperature, using junction-to-case thermal resistance.

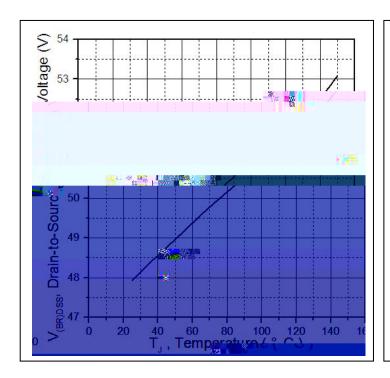
The value of $R_{\theta JA}$ is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with TA =25°C

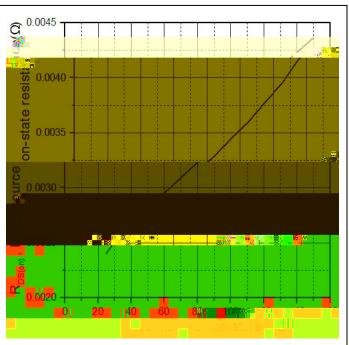




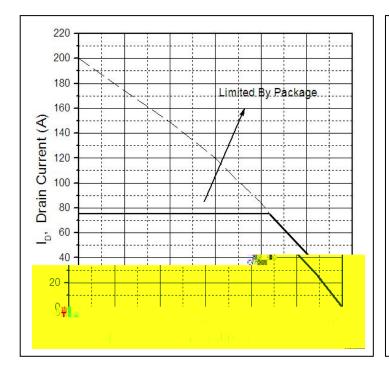


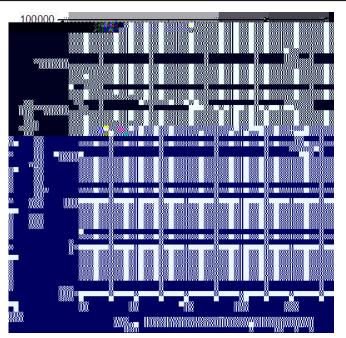
Gate to source cut-off Voltage

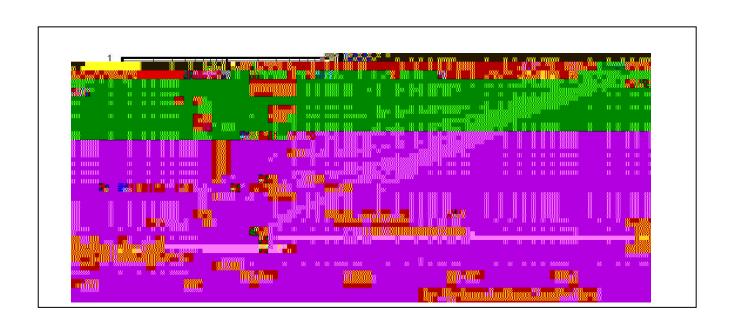




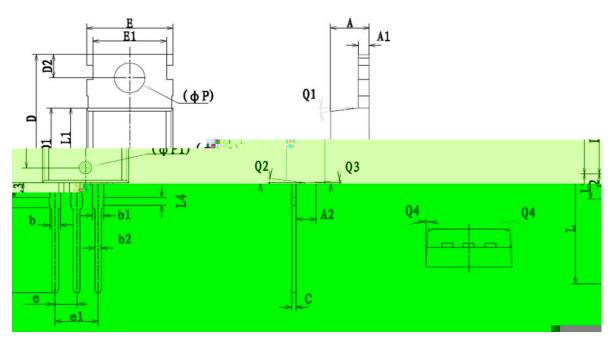


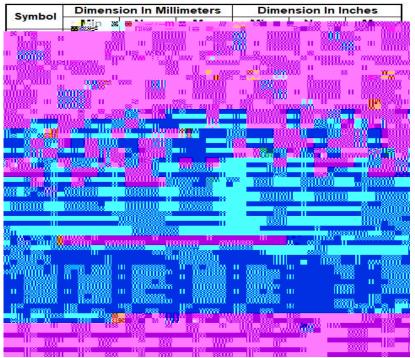




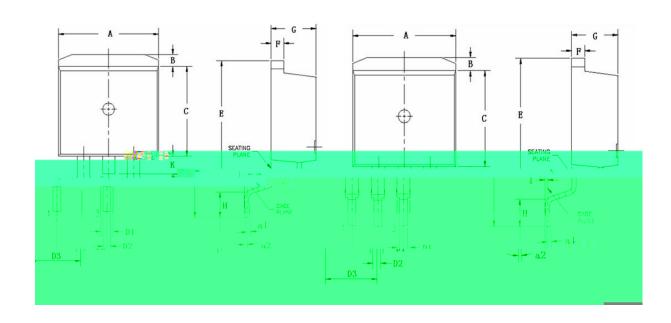


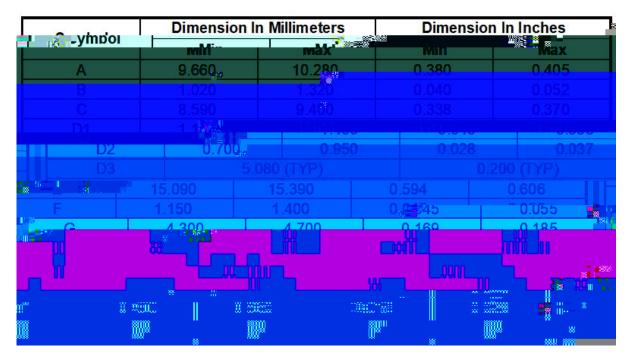














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