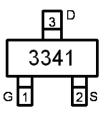
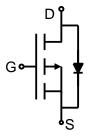


Main Product Characteristics:

V _{DSS}	-30V				
R _{DS} (on)	39 (typ.)				
I _D	-4.2A				







SOT-23

Marking and Pin Assignments

Schematic Diagram

Features and Benefits:

- Advanced MOSFET process technology
- Special designed for PWM, load switching and general purpose applications
- Ultra low on-resistance with low gate charge
- Fast switching and reverse body recovery
- 150 operating temperature
- Add HF for Halogen Free



Description:

It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

Absolute Max Rating: @T_A=25 unless otherwise specified

Symbol	Parameter	Max.	Units	
I _D @ TC = 25°C	Continuous Drain Current, V _{GS} @ 10V	-4.2		
I _D @ TC = 70°C	Continuous Drain Current, V _{GS} @ 10V	-3.5	Α	
I _{DM}	Pulsed Drain Current	-30		
P _D @TC = 25°C	Power Dissipation	1.4	W	
V _{DS}	Drain-Source Voltage		V	
V _{GS}	Gate-to-Source Voltage	±12	V	
T _J T _{STG}	Operating Junction and Storage Temperature Range	-55 to +150	°C	

Thermal Resistance

Symbol	Characterizes	Тур.	Max.	Units
R A	Junction-to-		90	°C /W



Electrical Characterizes $@T_A=25$ unless otherwise specified $c\Breve{A}\!D$

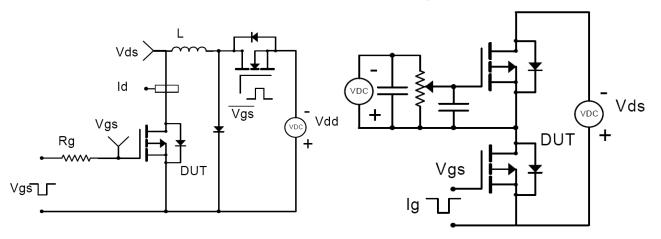
Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source breakdown voltage	-30			V	$V_{GS} = 0V$, $I_D = -$
			39	50		V_{GS} =-10 V , I_{D} = -4.2 A
R _{DS(on)}	Static Drain-to-Source on-resistance		48	65		V _{GS} =-4.5V,I _D = -4A
•	•	•	68	120	-	V _{GS} =-2.5V.I _D = -1A



Test Circuits and Waveforms

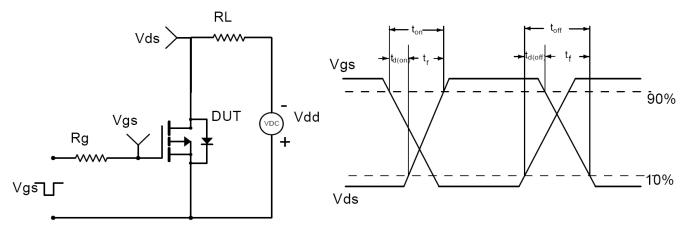
EAS Test Circuit:

Gate Charge Test Circuit:



Switching Time Test Circuit:

Switch Waveforms:



Notes:

Calculated continuous current based on maximum allowable junction temperature.

Repetitive rating; pulse width limited by max. junction temperature.

The power dissipation PD is based on max. junction temperature, using junction-to-case thermal resistance.

The value of R $_{\rm A}$ is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with TA =25°C



Typical Electrical and Thermal Characteristics

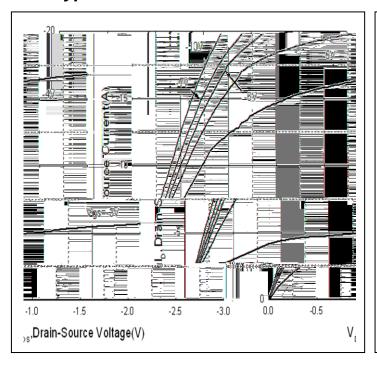
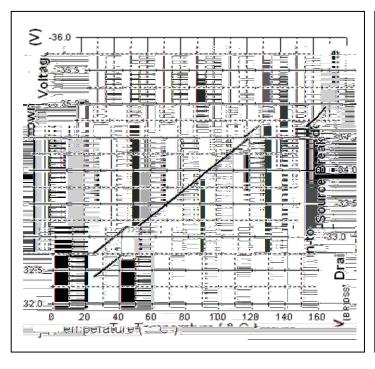




Figure 1 Typical Output Characteristics

Figure 2. Gate to Source Cut-off Voltage





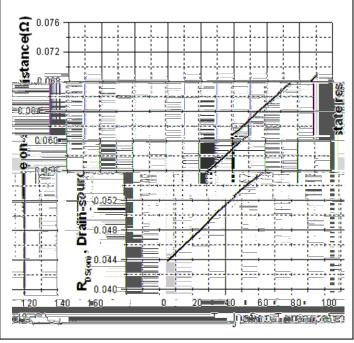
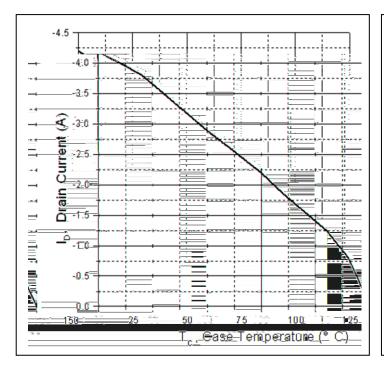


Figure 4. Normalized On-Resistance vs.

Junction Temperature



Typical Electrical and Thermal Characteristics



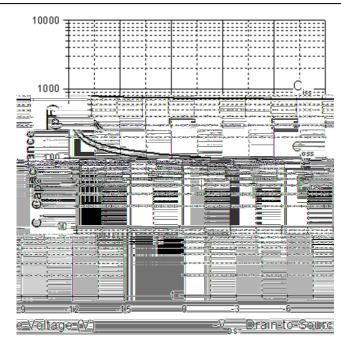


Figure 5. Maximum Drain Current vs. Case Temperature

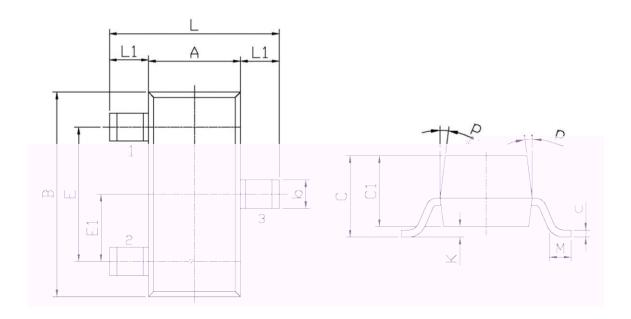
Figure 6. Typical Capacitance vs. Drain-to-Source Voltage



Figure 7. Maximum Effective Transient Thermal Impedance Junction-to-Case



Mechanical Data



Symbol	Dimensions In Millimeters		6 1-1	Dimensions In Millimeters	
	Min	Max	Symbol	Min	Max
L	2.2	2.7	С	1.30Ma×	
L1	0.45	0.65	C1	0.90	1.20
Α	1.15	1.50	C	0.05	0.20
В	2.70	3.10	. K	υ I	∪ე∙ի^
Ε	1.70	2.10	М	0.20MIN	
E1	0.85	1.05	Р	7*	
b	0.35	0,55			



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