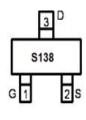
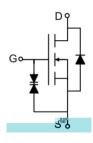


V _{DSS}	50V		
R _{DS} (on)	3.5Ω(Max)		
I _D	0.22A		







Advanced MOSFET process technology Special designed for PWM, load switching and general purpose applications Ultra low on-resistance with low gate charge Fast switching and reverse body recovery 150 operating temperature



It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

I _D @ T _C = 25°C	Continuous Drain Current, V _{GS} @ 10V	0.22	•	
I _{DM}	Pulsed Drain Current	0.88	A	
P _D @T _C = 25°C	Power Dissipation	0.43	W	
V _{DS}	Drain-Source Voltage	50	V	
V _{GS}	Gate-to-Source Voltage	± 20	V	
Tj Tstg	Operating Junction and Storage Temperature Range	-55 to +150	°C	



Reja	Junction-to-Ambient	—	350	/W

@T_A=25 unless otherwise specified

 $V_{(BR)DSS}$ Drain-to-Source breakdown voltage 50 — — V $V_{GS} = 0V$, $I_D = 250 \mu A$

 $R_{\text{DS(on)}} \qquad \text{Static Drain-to-Source on-resis}$

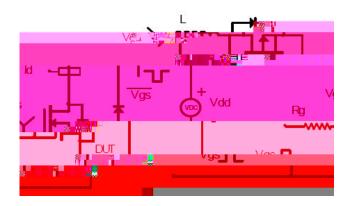
onS (

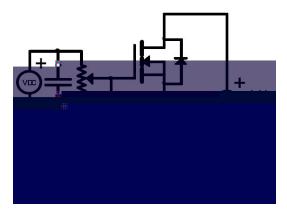
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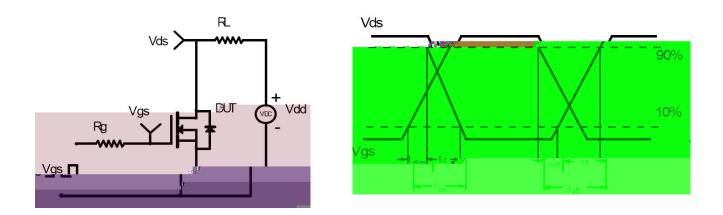
voltage

Vcs#56\$9db¼)25¢µ Dm"









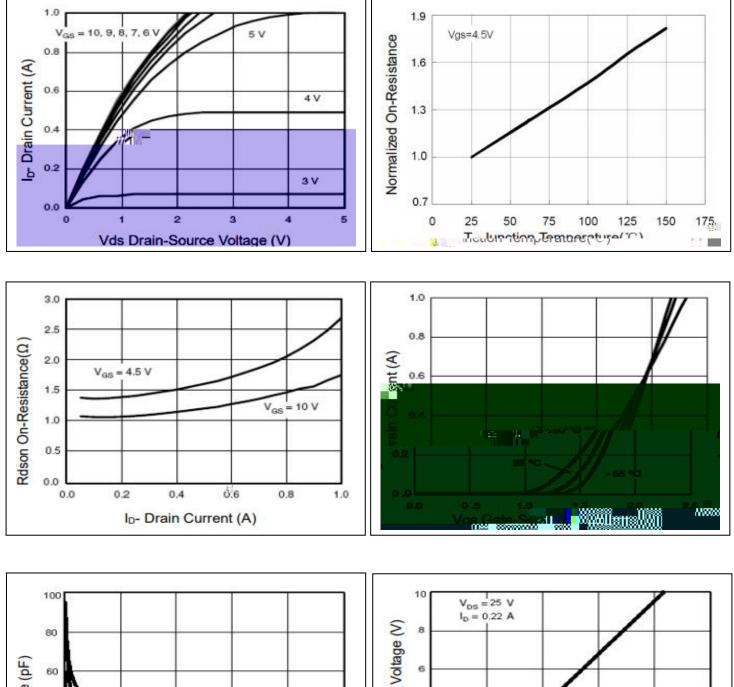
Calculated continuous current based on maximum allowable junction temperature.

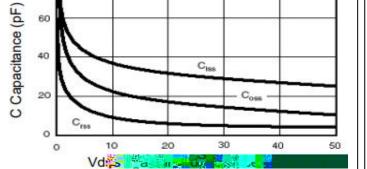
Repetitive rating; pulse width limited by max. junction temperature.

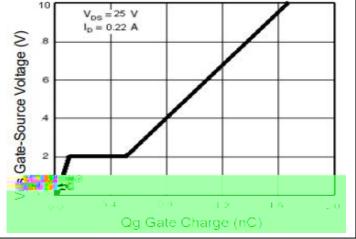
The power dissipation PD is based on max. junction temperature, using junction-to-case thermal resistance.

The value of $R_{\theta JA}$ is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with TA =25

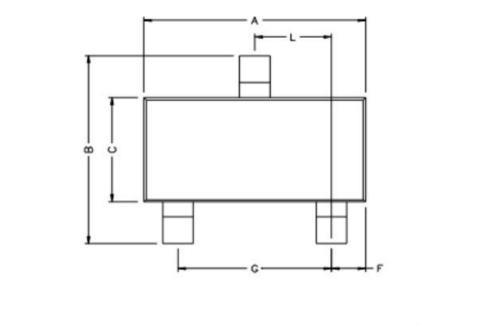














REF.	Millimeter		REF.	Millimete	
	Min	IVMR.Y		wMi:**	Max.
Α	2.80	3.00	G	1.80	2.00
В	2.30	2.50	Η	0.90	1.1
С	1.20	1.40	K	0.10	0.20
D	0.30	0.50	J	0.35	0.70
E	0	0.10	L	0.92	0.98
F	0.45	0.55	М	0°	10°

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